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## (12) ABSTRACT OF INVENTION

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(98) Mail address:  
117279, Moskva, ul. Miklukho-Maklaja 55a, AO  
TsPU "PATIS", Beljavskoj N.B.

(71) Applicant:  
Kushner Jurij Konstantinovich

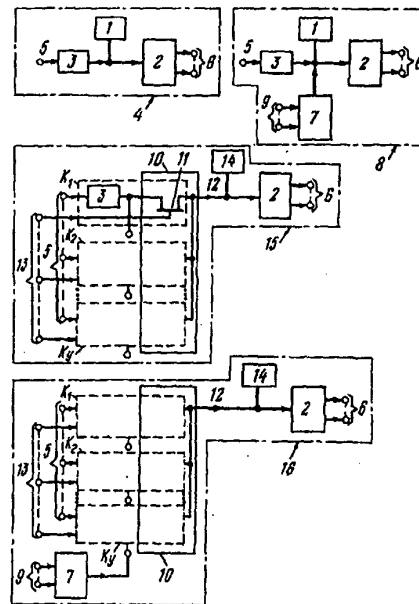
(72) Inventor: Kushner Ju.K.

(73) Proprietor:  
Kushner Jurij Konstantinovich

(54) METHOD FOR LINKING INDIVIDUALLY DESIGNED LOGIC CIRCUITS IN CUSTOM LARGE-SCALE INTEGRATED CIRCUIT

(57) Abstract:

FIELD: computer engineering. SUBSTANCE: method involves formation of set of individually designed logic circuits of simplest elements which are coincidence hardware assemblies of logic circuits, installation, arrangement, and tracing configurations of simplest elements. EFFECT: improved functions, configurations, and parameters of individually designed logic circuits. 1 dwg



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